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In addition to the
fundamental logic
design topics, we
address, in an integrated
manner, application-
specific logic structures
like PLDs; the impact

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and requirements of
VLSI technology;
testing issues and design
for testability; and the
theory needed to
understand such
important
implementation
technologies as tri-state
logic and CMOS
circuits.

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John P...

So now let's try to design a bit of circuitry using digital logic signals of 0 and 1, which will do addition. And so we're going to try to design a little six bit binary addition circuit. So I'm going to have as inputs, the six digits of the first binary number--a 5 down through a 0 and then the

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second binary number.
Let's call it b_0 through b_5 .

Digital Logic | 1.4 Logic & Propositions | 1.4 Logic ...

Question: QUESTION 4
Design One
Applications Which Can
Be Used Digital Logic
Function In Its
Operation. Include
Block Diagram, Circuit

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Description In Your
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**QUESTION 4 Design
One Applications
Which Can Be Us ...**

Most digital logic is synchronous because it is easier to create and verify a synchronous design. However, asynchronous logic has the advantage of its

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speed not being constrained by an arbitrary clock; instead, it runs at the maximum speed of its logic gates.

Building an asynchronous system using faster parts makes the circuit faster.

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